CURRICULUM VITAE

DEZSŐ SIMA

Personal data:

Born:	Kassa, May 27, 1941
Affiliation:	Obuda University, John von Neumann Faculty of Informatics
Position:	professor
Phone:	36-1-666-5507
E-mail:	sima@uni-obuda.hu
Fax:	36-1-666-5522

Education:

1962-1965	Technische Universität Dresden, Fakultät für Schwachstromtechnik
1959-1962	Technical University Budapest, Faculty of Electrical Engineering

Degrees:

DSc	(Hungarian Academy of Sciences, 2004)
Dr.-Ing	(TU Budapest, 1972)
CSc	(Hungarian Academy of Sciences, 1972)
Dr.-Ing	(TU Dresden, 1971)
Dipl-Ing.	(TU Dresden 1965)
	Dipl.-Ing. Schwachstromtechnik
	Fachrichtung: Hochfrequenztechnik (1965)

Language certificates:

English-Deutsches Fachübersetzer Zeugnis, TU Dresden, 1968,
Fluent in English and German

Professional affiliations:

2010-	Obuda University (previously Budapest Tech)
2000-2009	Budapest Tech, John von Neumann Faculty of Informatics, dean (2000-2006), founding dean (2006-)
1979-1999	Kálmán Kandó Polytechnic, Institute of Mathematics and Computer Engineering, director
1972-1979	Kálmán Kandó Polytechnic, Department of Computer Engineering, chair
1966-1972	Technische Universität Dresden, Section Informationstechnik, research fellow, lecturer

Guest professorships:

1992/1993	South Bank University London (one semester)
1985/1986	Technische Universität Dresden (one semester, Barkhausen Professorship)

Create PDF files without this message by purchasing novaPDF printer (http://www.novapdf.com)

PhD supervisorships:
3 PhD students (Technische Universität Dresden and Hungarian Academy of Sciences)

Selected R&D and educational projects (mostly as project leader):

2008-2010	Acceleration of Financial Computations by GPGPUs (contracted by Morgan Stanley, Hungary)
2008-2009	Performance Investigations on Multicores (contracted by IBM Deutschland, Böblingen Lab)
2005-2007	Development of an Intelligent Knowledge Assessment System (contracted by IBM Hungary)
1997-2000	Quality Assurance System and Credit Transfer System, TEMPUS-S-JEP-12435-97
1997-2000	Distributed and Network Computing in the Information Society, TEMPUS S-JEP-12495-97
1994-1997	Introducing Parallel Processing into the Curriculum of the Hungarian Higher Educational Institutions of Technology, TEMPUS S-JEP-08333-94
1992-1993	cCLEAN – recognition of text in paper based technical drawings (in cooperation with the Institut für Grundlagen der Informationsverarbeitung und Computer gestützte neue Medien der Technischen Universtität Graz)
1991-1994	PROMISE: Promotion of Mobility and Informatics Studies in Europe, TEMPUS JEP-2309-91 (180.900 ECU, as project contractor)
1990-1991	PROFESSOR / automatic recognition of electronic diagrams
1988-1990	Development of the TEACHSOFT CAE mathematical program package Version 2.
1981-1983	Development of the TEACHSOFT CAE mathematical program package Version 1.

Participation in professional organizations and societies:

2008-2010	President of the Committee of Technical Disciplines of Hungarian Scholarship Board
2000-2003	Hungarian Accreditation Board (MAB), vice president
1997-2003	Hungarian Accreditation Board (MAB), member
1995-	IEE Fellow
1994-2000	John von Neumann Society of Computing (NJSZT), President
1991-	IEEE member
1985-	John von Neumann Society of Computing (NJSZT), member of Presidium
1972-	John von Neumann Society of Computing (NJSZT), member

Participation in international accreditation

2006/2008/2009	Accreditation of informatics programs at Tallin Technical University and Colleges (Estonia)
2007	Accreditation of the Austrian Agency for Quality Assurance (AQA)

Participation in the Hungarian Bologna Process:

2003	Co-chair of the Bologna Committee of Informatics
2003	Co-chair of the Bologna Committee of Hungarian Higher Educational Institutions
2002	Member of the Hungarian Bologna Committee
2002	Chair of the Bologna Committee of Hungarian Colleges and Politechnics

Participation in Conference Programme Committees:

2007	First Regional Conference on Embedded and Ambient Systems (RCEAS 2007), 2007, Sárospatak (International Programme Committee member)
2007	XI. EuroLogo Konferencia 2007, Pozsony (Honorary Chair)
2007	Alkalmazott Informatika Konferencia 2007, Kaposvár (Programme Committee member)
2006	IX. Országos Neumann Kongresszus 2006, Győr (Programme Committee member)
2005	Informatika a Felsőoktatásban Konferencia 2005, Debrecen (Programme Committee member)
2003	Informatika a Felsőoktatásban Konferencia 2003, Debrecen (Programme Committee member)
1998	DAPSYS'98 (Programme Committee member)
1997-1998	15th World Computer Conference IFIP'98 (Programme Committee member, Advisory Board president)
1996	2. Informatika a Felsőoktatásban Konferencia, Debrecen (Programme Committee member)
1993	Informatika a Felsőoktatásban Konferencia, Debrecen (Programme Committee member)
1993	The Challenge of Networking, 8. Joint Hungarian-Austrian Informatics Conference, Szombathely (Programme Committee president)

Membership in Editorial Boards:

2002-	Gikof Journal
1993-1999	Infopen
1993-	J. UCS (Journal of Universal Computer Science, Springer)

Awards:

2006	Order of Merit of the Hungarian Republic - Knight Cross
2006	John von Neumann Award
2003	Apáczai Csere János Prize
1999-2001	Széchenyi professorship
1993	John von Neumann medal
1990	Kandó Memorial ring
1989	Tarján Rezső Prize

Selected publications:

Books:

Sima D., Fountain T., Kacsuk P.: Advanced Computer Architectures, A Design Space Approach, Addison-Wesley Publishing Co, Harlow etc. 1998, 766 pages.

Sima D., Fountain T. és Kacsuk P. (in Hungarian, translation of [1]): Korszerű számítógép architektúrák - Tervezési tér megközelítésben. SZAK Kiadó, Budapest, 1998, 809 pages.

Sima D., and Haring G.: (eds.:) The Challenge of Networking: Connecting Equipment, Humans, Institutions, München 1993, R. Oldenbourg Kiadó, 265 pages.

Book chapters:

Register Renaming Techniques, in Computer Engineering Handbook, CRC Press LLC, 2001, pp. 6.6-6.30.

Journal papers:

Sima D.: Korszakváltás a processzorok fejlődésében, *Alkalmazott Matematikai Lapok*, Vol. 24, 2007. 265-275. old.

Sima D.: „Decisive Aspects in the Evolution of Microprocessors", Proc. IEEE, vol. 92, no. 12, 2004, pp. 1895-1926.

Sima D.: The Design Space of Register renaming Techniques, IEEE Micro, Vol. 45, No. 11, Sep/Oct 2000. pp. 70-83.

Sima D.: The Design Space of Shelving, Journal of Systems Architecture Vol. 45, No. 11. pp. 863-885.

Sima D.: Superscalar Instruction Issue, IEEE Micro, Vol. 17, No. 5, Sept/Oct., 1997. pp. 28-39.

Proceedings:

D. Sima, B. Schmuck, S. Szöllősi, Á. Miklós: Intelligent Short Text Assessment in eMax, *IEEE Africon 2007,* Windhoek Namíbia, ISBN: 0-7803-8606-X.

L. Csink, A. György, Zs. Raincsák, B. Schmuck, D. Sima, Zs. Sziklai and S. Szöllösi: „Intelligent Asessment Systems for e-Learning", *Proc. 4th European Conference on E-Activities*, 2003, Bucharest, 2003, pp. 224-229.

Miklós Á. and Sima D.: „VSIM – A Superscalar CPU Simulator, „Proc. 3nd International Conference on Information Technology Based Higher Education and Training (ITHET 2002), Budapest, 2002, pp. 561-572.

Sima D.: Some Aspects of the Functional Evolution of ILP-processors, *Proc. International Conference on Information Infrastructure, ICII '96*: Beijing, 1996, pp. 130-133.

Last updated: 09.2010